## Audio IC Application Circuit

## TAN-321

## Application Circuit Example of 3-band (FM, MW and LW) 5-V Tuner TA2149BN + TC9257F

## 1. Outline



The above is an application circuit example of a 3 -band (FM, MW and LW) 5 -V tuner.
A separate $9-\mathrm{V}$ power supply is required for V tune operation.
The circuit incorporates the single-chip TA2149BN, and flat-type TC9257F for PLL block.

## 2. Ratings

| Characteristic | Rating |  |  |
| :---: | :---: | :---: | :---: |
|  | FM | MW | LW |
| Supply Voltage | 5 V |  |  |
| Signal Frequency Range | 87.5~108 MHz | $522 \sim 1620 \mathrm{kHz}$ | 153~281 kHz |
| Intermediate Frequency | 10.7 MHz | 450 kHz |  |
| Sensitivity | $\begin{gathered} 17.5 \mathrm{~dB} \mu \mathrm{~V} \mathrm{EMF} \\ (\mathrm{~S} / \mathrm{N}=30 \mathrm{~dB}) \end{gathered}$ | $\begin{gathered} 32 \mathrm{~dB} \mu \mathrm{~V} / \mathrm{m} \\ \left(\mathrm{~V}_{\mathrm{o}}=10 \mathrm{mV} \mathrm{rms}\right) \end{gathered}$ | $\left.\begin{array}{c} 42 \mathrm{~dB} \mu \mathrm{~V} / \mathrm{m} \\ \left(\mathrm{~V}_{\mathrm{o}}=10 \mathrm{mV}_{\mathrm{rms}}\right. \end{array}\right)$ |

## 3. MW-LW Block

## 1) MW-LW circuit

The coil used in the oscillation circuit is designed for MW. For LW the oscillation frequency is lowered by attaching a capacitor in parallel. A transistor is used to allow switching between MW and LW.
In the same way, use of a transistor allows the antenna coil to be switched between MW and LW too.

## 2) MW-LW adjustment

<f cover adjustment>
MW: The coil should be adjusted so that VT be 8 V at 1620 kHz (approximately 1.9 V at 522 kHz ).
LW: The 30 pF trimmer should be adjusted so that VT be 6 V at 281 kHz .
<MW/LW sensitivity adjustment>

1) Adjust tracking for MW at 600 kHz and 1400 kHz .
2) Adjust tracking for LW using a sweep generator so that sensitivity becomes highest around $f$ cover area.
3) Adjust tracking for MW again so as to offset the effects of LW antenna coil.

## 3) TC9257F Software Specifications

```
<X'tal>
    4.5 MHz
<IF terminal>
    IF IN2 (16 pin)
<I/O ports>
    1) OT-4 (9 pin) : FM "open", MW. LW "L"
    2) I/O-5 (10 pin) :FM "L" ,MW. LW "H"
    3) I/O-6 (11 pin) : IF request "H" , Normal tuning "L"
    4) I/O-7 (18 pin):MW "H" , LW "L"
    5) I/O-8 (17 pin) : LW "H" ,MW "L"
<Reference frequency>
    FM : 50 kHz
    MW : 9 kHz
    LW : 1 kHz
```

TOSHIBA

## TA2149BN-3 Band (FM, MW and LW)

Coil Specification

| No. | Stage | $\begin{gathered} f \\ (\mathrm{~Hz}) \end{gathered}$ | $\begin{gathered} \mathrm{L} \\ (\mu \mathrm{H}) \end{gathered}$ | $\begin{gathered} \mathrm{C} \\ (\mathrm{pF}) \end{gathered}$ | Q | Turns |  |  |  |  | Wire (mm) | Note |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  | 1-2 | 2-3 | 1-3 | 1-4 | 4-6 |  |  |
| L1 | FM RF | 100M | - | - | 79 | - | - | - | 21/2 | - | 0.16 UEW | 666SNF-305NK@T |
| L2 | MW ANT | 796k | 285 | - | 200 | 69 | - | - | $\begin{gathered} 13 \\ (3-4) \end{gathered}$ | - | 7/0.07 USTC | MSE-0118-5@M |
| L3 | LW ANT | 252k | 1660 | - | 100 | - | $\begin{gathered} 160 \\ (2-6) \end{gathered}$ | $\begin{gathered} 16 \\ (7-3) \end{gathered}$ | - | - | 4/0.07 USTC | MSE-0118-5@M |
| L4 | FM OSC | 100M | - | - | 76 | - | - | - | 2 | - | 0.16 UEW | 666SNF-306NK@T |
| L5 | MW OSC | 796k | 120 | - | 120 | 13 | 56 | - | - | - | 0.07 UEW | A7BRS-12552Y@T |
| T1 | MW IFT | 455k | - | 470 | 60 | - | - | 109 | - | 7 | 0.05 UEW | 5PLG-5147X@T |

M: MITSUMI (SEGAMAT) SDN. BHD
T: Toko, inc
(Bottom view)


## Description of Operation

In the printed circuit board pattern example, the oscillation at the lower frequency and the lower impedance may become parasitic oscillation frequency because the distance from this IC to the FM and RF tank circuits is long.

This tendency is particularly evident in $1 / 16$ VCO mode.
To prevent the parasitic oscillation, insert a resistor of around $7.5 \Omega$ between pin 21 and L4.
This measure reduces parasitic oscillating impedance, narrowing the parasitic oscillation range. The FM and RF tank circuits should be located as near the IC as possible.

Example of Printed Circuit Board Pattern



FM OUTPUT - $\Delta f$






## RESTRICTIONS ON PRODUCT USE

- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.
- The products described in this document are subject to the foreign exchange and foreign trade laws.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.

